

FIG. 1 and 2 are schematic diagrams of a system for transmitting and receiving signals.

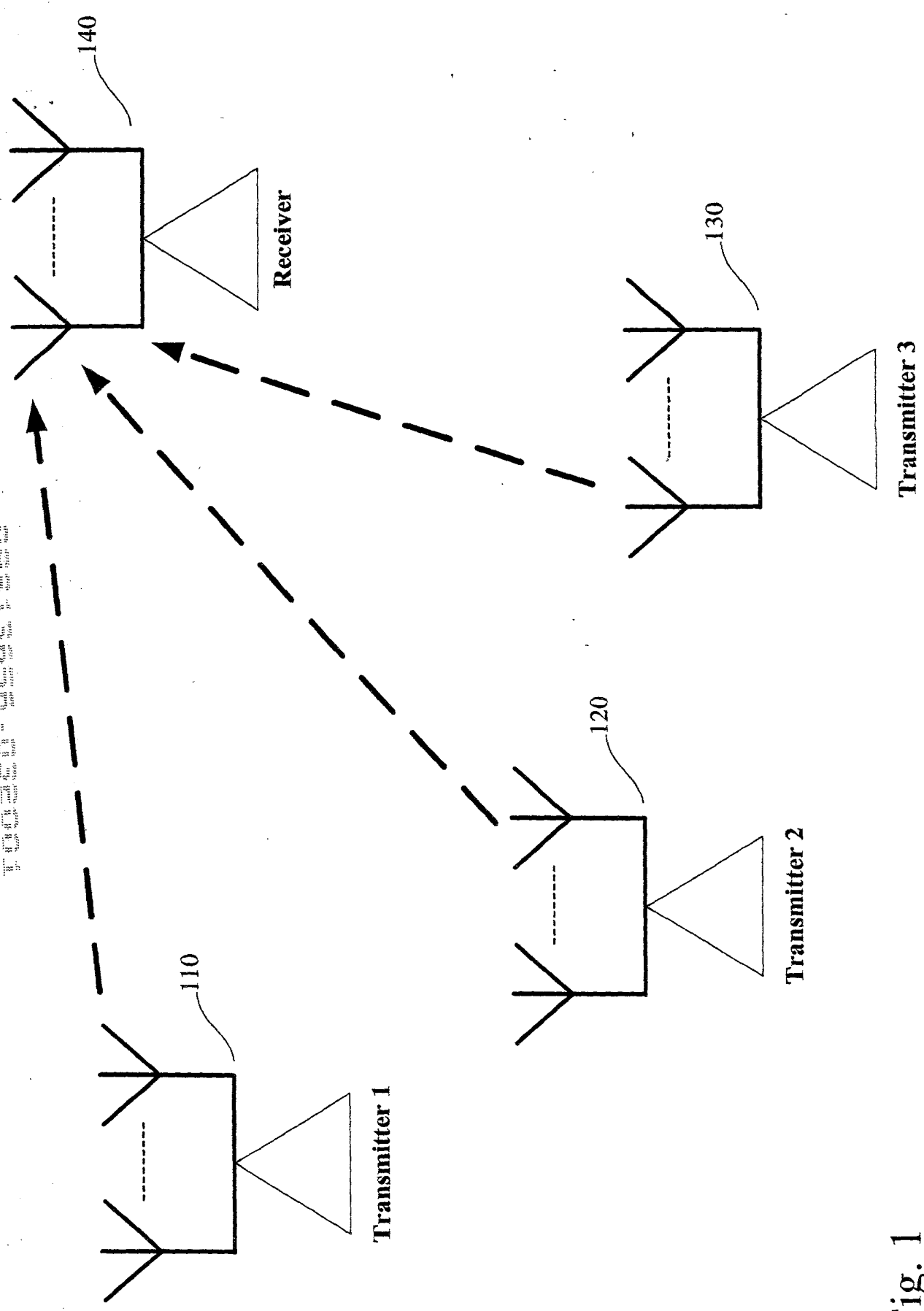


Fig. 1
(Prior Art)

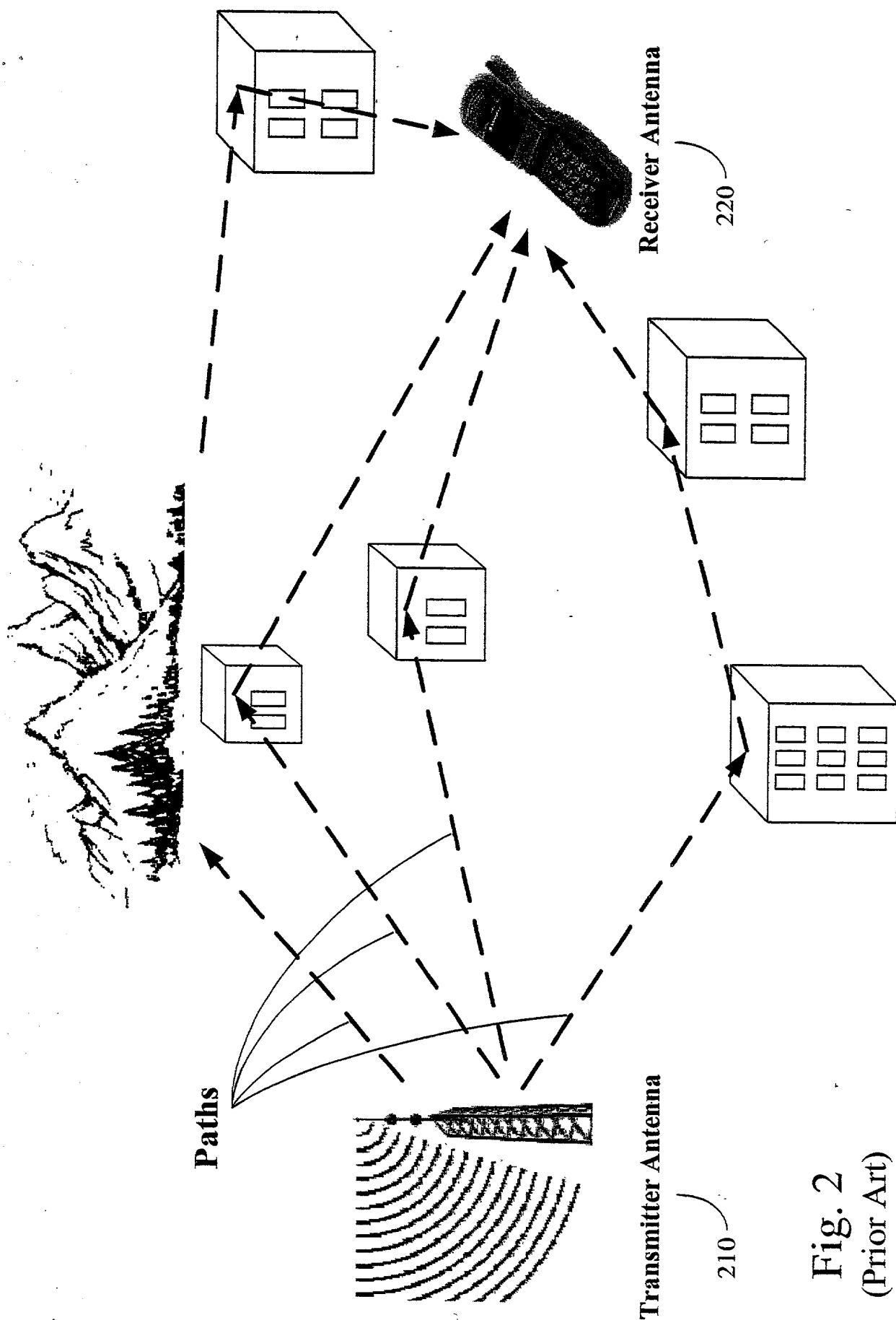


Fig. 2
(Prior Art)

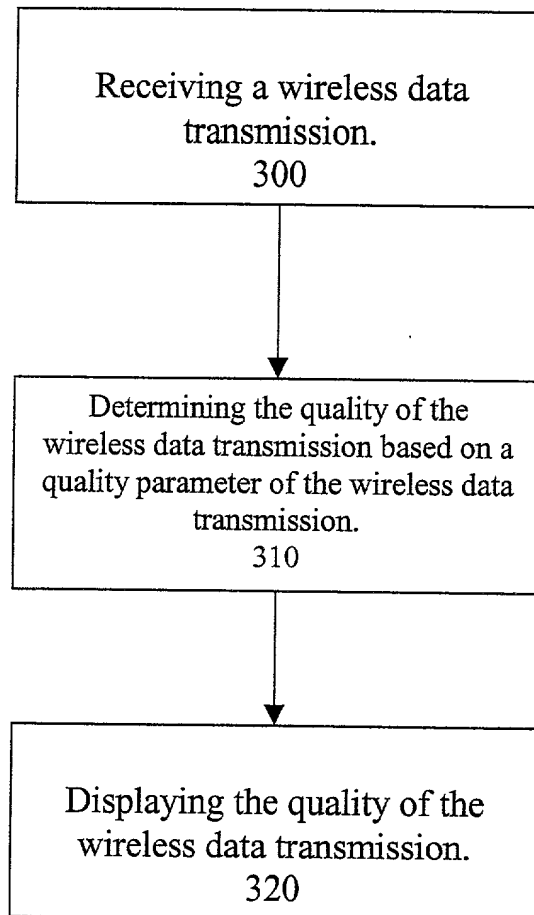


Figure 3

400

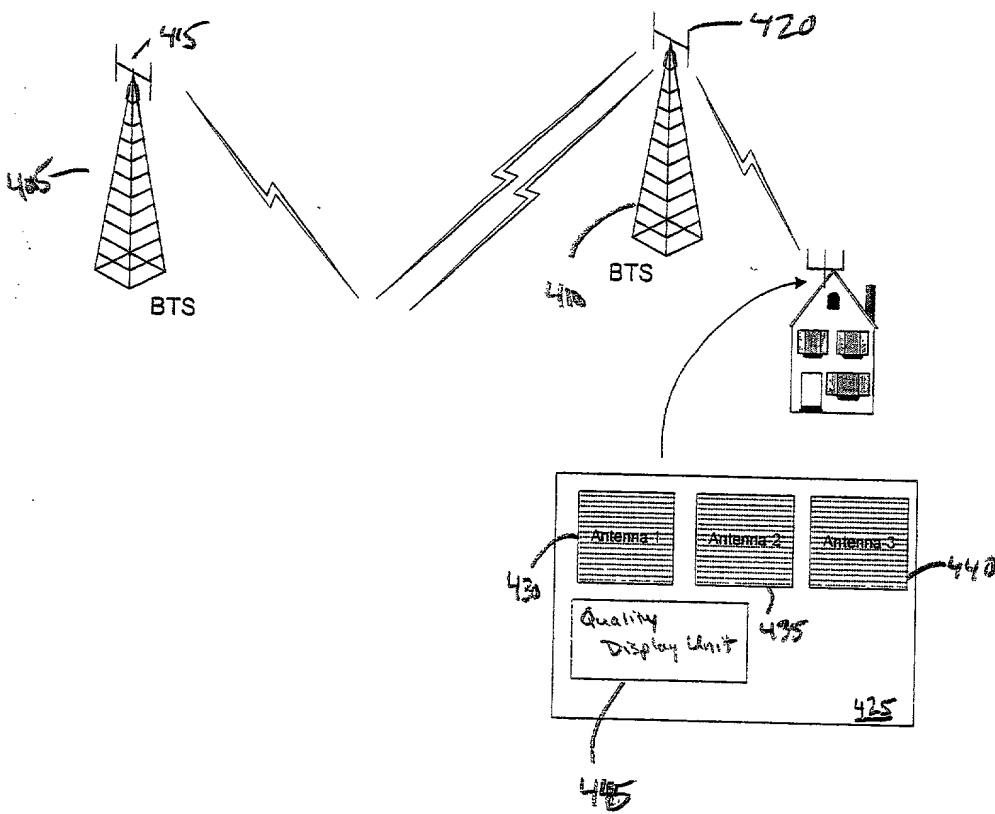


Figure 4A

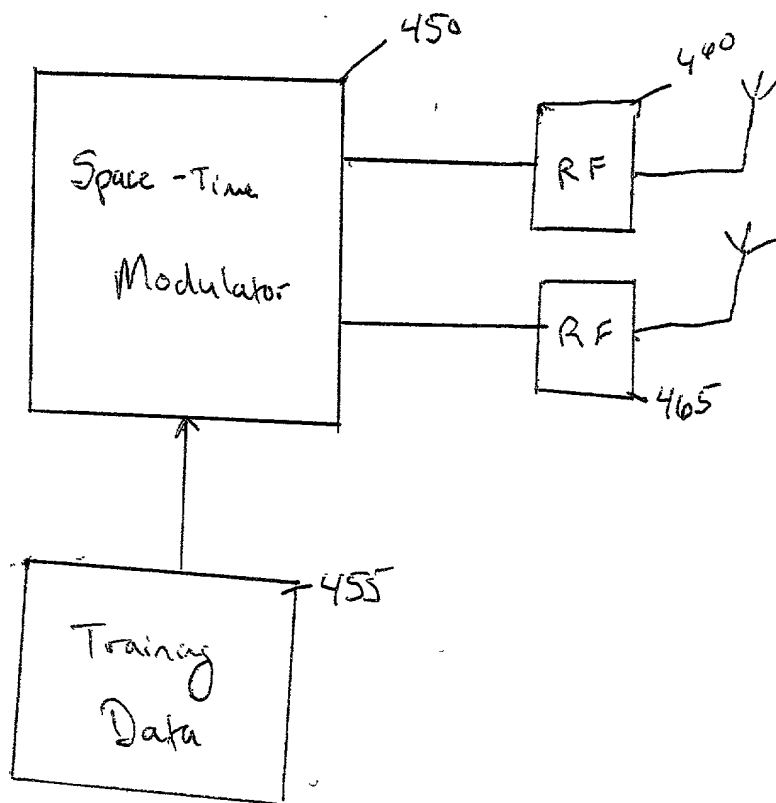


Figure 4B

500

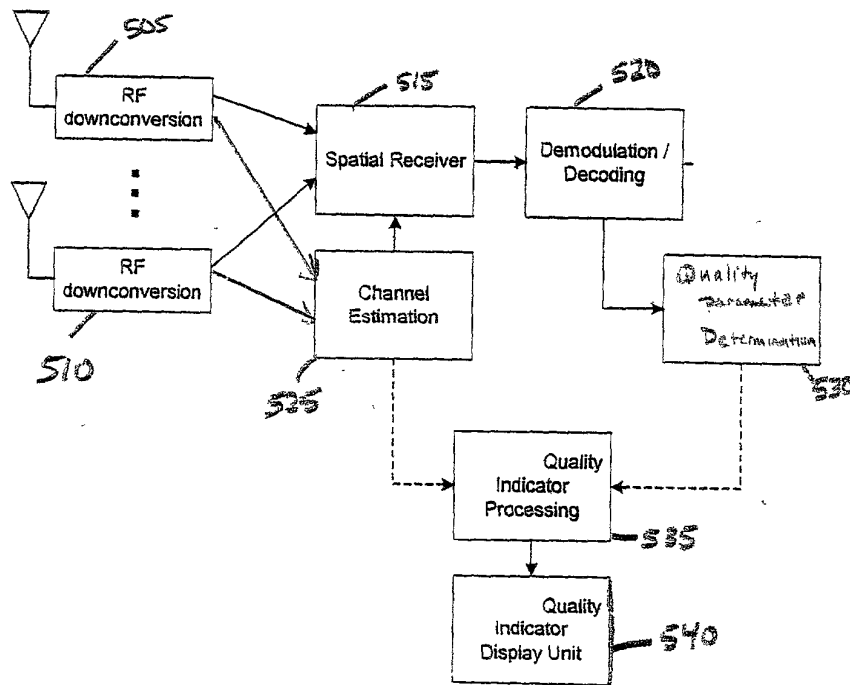


Figure 5

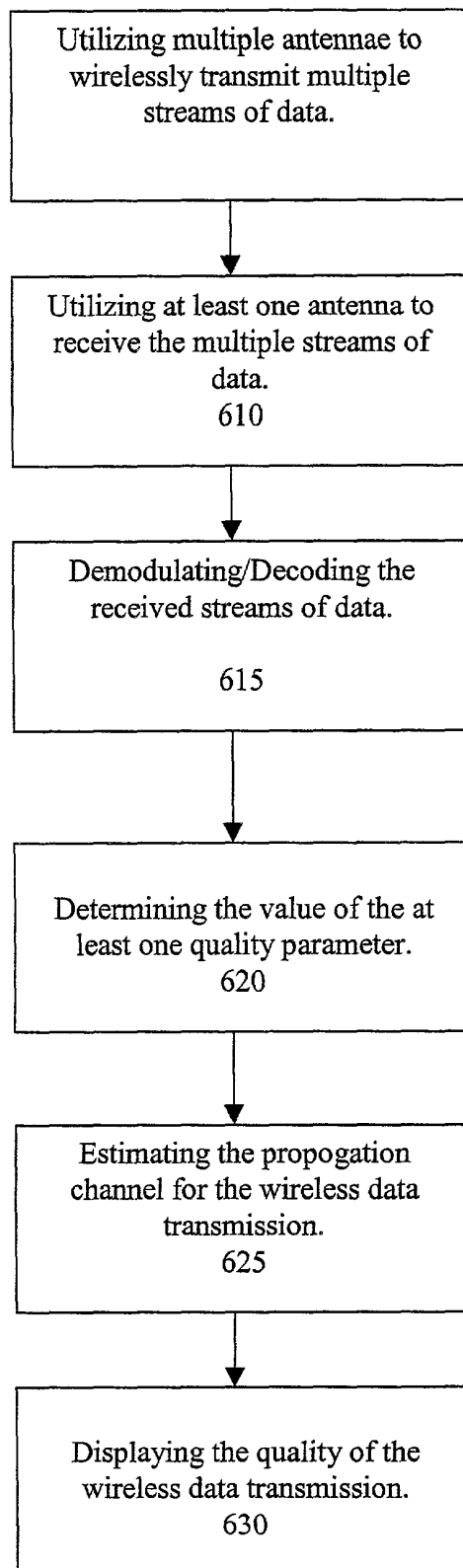


Figure 6

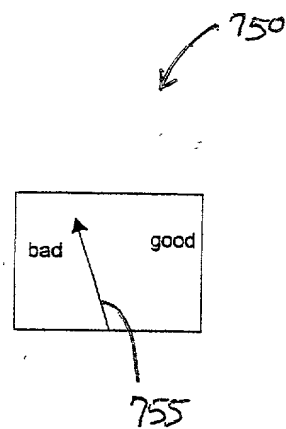
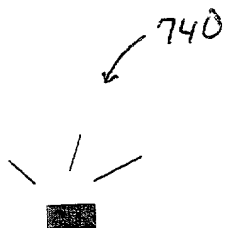
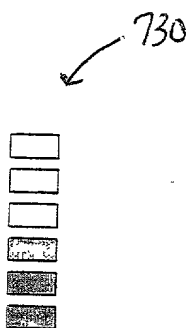
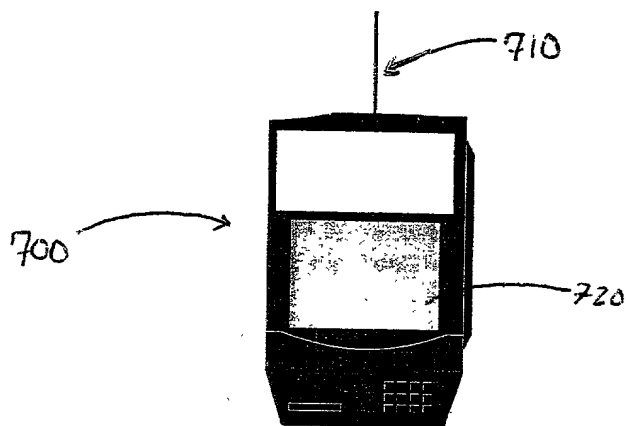


Figure 7

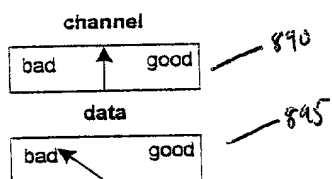
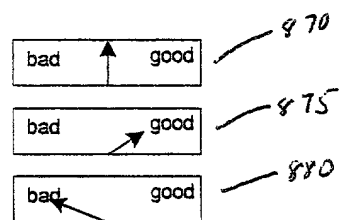
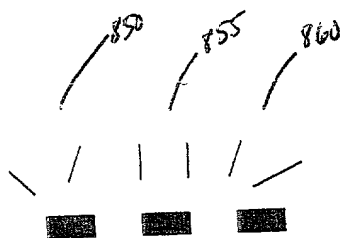
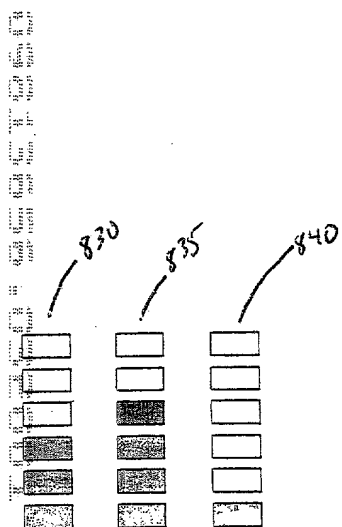
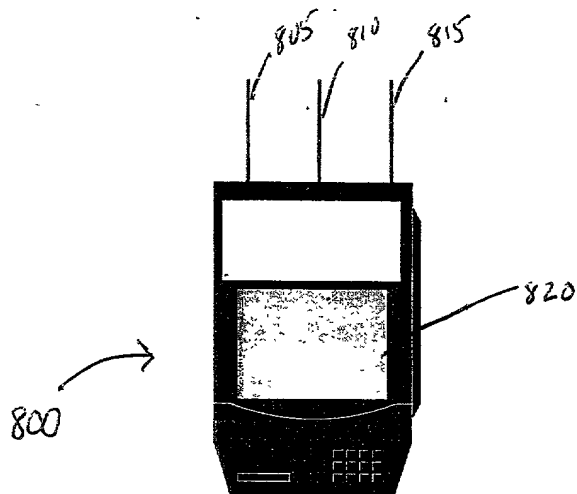


Figure 8